



PUMA 84FV1000006 - 90/12/15

Issue 1.0 March 2002

32M x 32 FLASH Module

## Description

The PUMA 84 range of devices provide a high density, surface mount memory solution with density up to twice that of standard monolithic devices.

The PUMA 84 may accommodate various memory technologies including SRAM, FLASH and EEPROM. The devices are designed to offer a defined upgrade path and may be user configured as 8, 16 or 32 bits wide.

The PUMA 84FV1000006 is a 32M x 32, 3.3V FLASH Module in a 84 'J' Leaded package which complies with the JEDEC 84 PLCC standard.

Access times of 90/120/150 ns are available.

The 3.3V device is available to commercial and industrial temperature grade.

## Features

- Access times of 90, 120 and 150ns.
- 3.3V  $\pm$  10%  $V_{CC}$ .
- Commercial and Industrial temperature grades
- JEDEC Standard 84 'J' Lead surface mount package.
- May be organised as 32M x 32, 64M x 16 and 128M x 8
- Flexible Sector Architecture.
- Embedded Algorithms.
- Multiple Ground pins for maximum noise immunity.

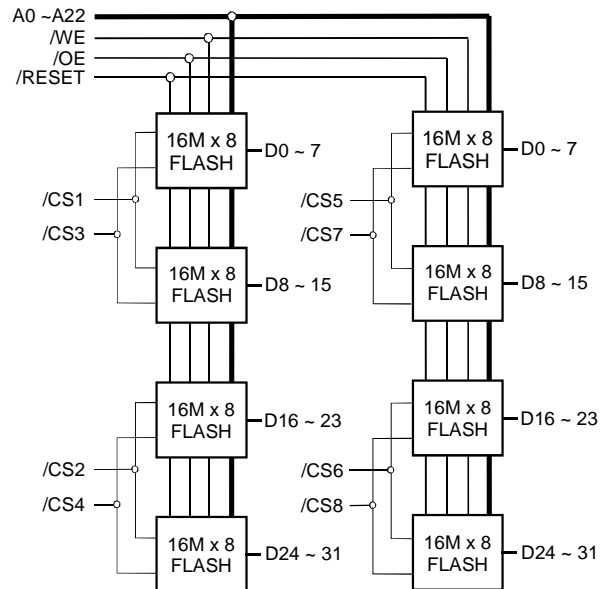
## Package Details

PUMA 84 - Plastic 84 'J' Leaded Package.

Max. Dimensions - 30.35 x 30.35 x 5.7mm

All Dimensions in mm.

## Block Diagram



## Pin Definition

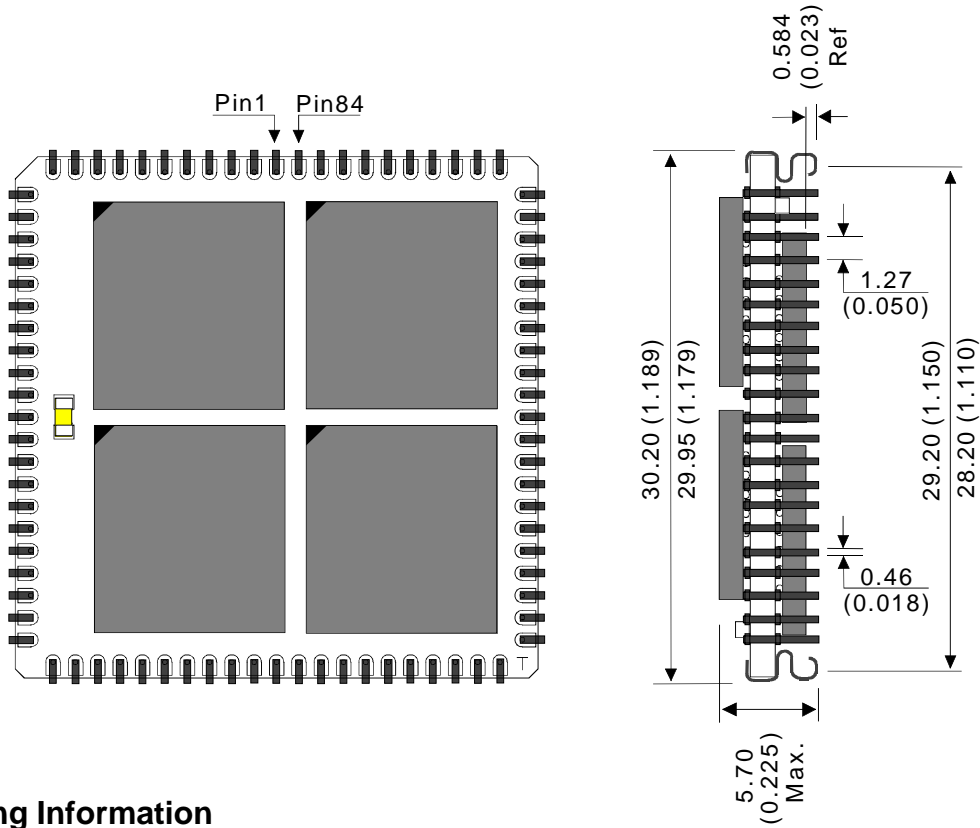
To be defined

## Pin Functions

Description	Signal
Address Input	A0~A22
Data Input/Output	D0~D31
Chip Select	/CS1~8
Write Enable	/WE
Output Enable	/OE
Hardware Reset	/RESET
No Connect	NC
Power	$V_{CC}$
Ground	$V_{SS}$

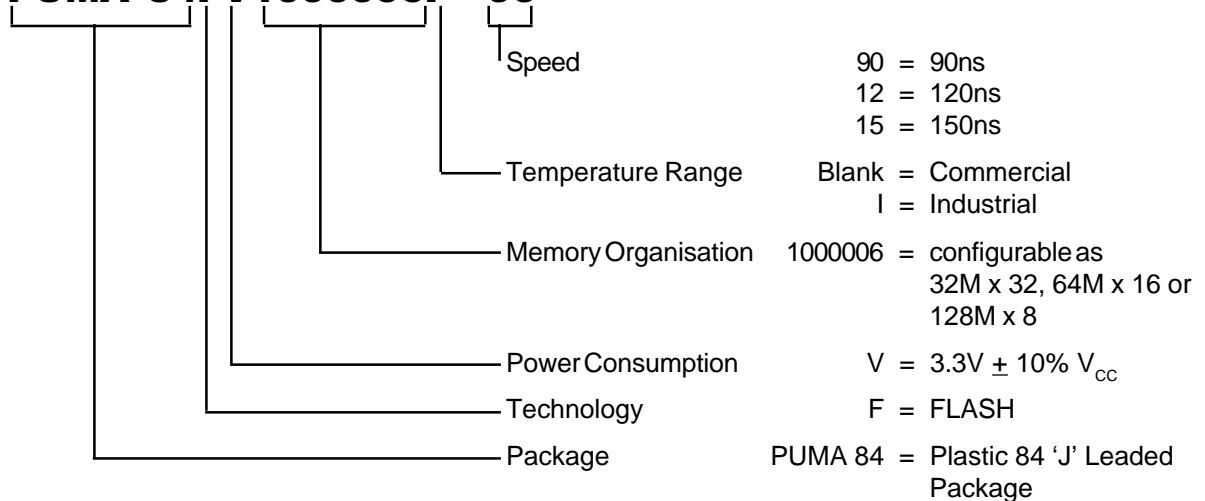
### Package Details

PUMA 84 - Plastic 84 'J' Leaded Package.



### Ordering Information

#### PUMA 84FV1000006I - 90



**Note :**

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